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REMARKS

This amendment is being made pursuant to 37 C.F.R. §1.312. Accordingly, all pending claims in the instant application are allowed. Claim 180 has been amended to improve its clarity. No new matter has been added. Applicant's respectfully request entry of the foregoing amendment.

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Respectfully submitted,



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Version With Markings to Show Changes Made

1 180. (twice amended) An integrated circuit device comprising:
2 input receiver circuitry to sample an operation code
3 synchronously with respect to a first transition of an external
4 clock signal, the operation code specifying a read operation; and
5 output driver circuitry to output data in response to the
6 operation code [specifying a read operation], wherein:
7 the output driver circuitry outputs a first portion of
8 data in response to a rising edge transition of the external
9 clock signal; and
10 the output driver circuitry outputs a second portion of
11 data in response to a falling edge transition of the external
12 clock signal.